

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/577,677	HE ET AL.	
Examiner Howard Williams		Art Unit 2819	Page 1 of 1	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2008/0001793 A1	01-2008	Ghoneima et al.	341/100
*	B	US-2007/0290902 A1	12-2007	Bae et al.	341/101
*	C	US-2007/0226387 A1	09-2007	Croxford, Daren	710/066
*	D	US-2004/0153927 A1	08-2004	Pappalardo et al.	714/735
*	E	US-6,943,706 B2	09-2005	Pappalardo et al.	341/50
*	F	US-6,583,735 B2	06-2003	Henkel et al.	341/51
*	G	US-6,034,879 A	03-2000	Min et al.	365/63
H	US-				
I	US-				
J	US-				
K	US-				
L	US-				
M	US-				

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stan et al.; Low Power Encodings for Global Communications in CMOS VLSI; IEEE Transactions of Very Large Scale Integration Systems, (VLSI); IEEE Dec. 1997, Vol. 5, No. 4, pages 444-455
	V	Lyuh et al.; Low Power Bus Encoding with CrossTalk Delay Elimination; 15th Annual IEEE International Conference on ASIC/SOC; Sept 2002; pages 389-393
	W	Lee, Kangmin et al.; SILENT: Serialized Low Energy Transmission Coding for On-Chip Interconnection Networks; IEEE/ACM International Conference on Computer Aided Design, 2004. ICCAD-2004. Nov. 2004, page(s): 448- 451
	X	Ghonemia et al., Reducing the Data Switching Activity on Serial Link Buses; 7th International Symposium on Quality Electronic Design, 2006. ISQED '06; 27-29 March 2006, page(s): 6 pp.-432

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.